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11/14/2005 CNGUYEN1 00000091 201430 10696209

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J. Matthew Zigmant (Depositor's name)
J. Matthew Zigmant (Signature)
 November 11, 2005 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/696,209 | 10/28/2003 | Philip Y. Pan | 015114-053112US | 7337 |

TITLE OF INVENTION: DUAL-PORT SRAM IN A PROGRAMMABLE LOGIC DEVICE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$0 | \$1400 | 12/31/2005 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|--------------|----------|----------------|
| PHUNG, ANH K | 2824 | 365-230050 |

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

ALTERA CORPORATION

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

SAN JOSE, CA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

J. Matthew Zigmant
 J. Matthew Zigmant

Date November 11, 2005

Typed or printed name

Registration No. 44,005

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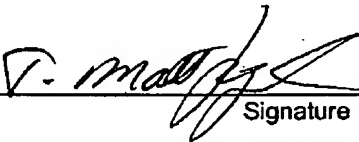


PTO/SB/97 (08-0-1)

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In re: application of: Philip Y. Pan et al.
Application Number: 10/696,209
Filed: 10/28/2003
Title: Dual-Port SRAM in a Programmable Logic Device
Atty Docket Number: 015114-053112US JMZ/ssb

Being faxed to the **USPTO, Mail Stop Issue Fee** at facsimile number **1-571-273-2885** are the following documents:

1. This PTO/SB/97 Certificate of Transmission (1 page); and
2. PTOL-85 Part B - Fee(s) Transmittal (1 page submitted in duplicate).

Number of pages being transmitted: 3

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